

Electronic Patent Application Fee Transmittal

Application Number:	10583538
Filing Date:	
Title of Invention:	Noise-reducing transistor arrangement, integrated circuit, and method for reducing the noise of field effect transistors
First Named Inventor/Applicant Name:	Ralf Brederlow
Filer:	Laura C. Brutman/Inga Hildreth
Attorney Docket Number:	V0195.0080

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Claims in excess of 20	1615	11	50	550
Miscellaneous-Filing:				
Oath/decl > 30 months from priority date	1617	1	130	130
Eng. Transl. > 30 mo. from priority date	1618	1	130	130
Petition:				
Patent-Appeals-and-Interference:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Miscellaneous:				
	Total in USD (\$)			810